

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (original) System for converting an analog wanted signal into a digital wanted signal and for suppressing an analog unwanted signal and comprising
analog filter for at least partly suppressing the analog wanted signal and the analog unwanted signal, resulting in an analog output signal;
an analog-to-digital-converter for converting the analog output signal into a digital output signal; and
a compensator for compensating the digital output signal for the at least partly suppressing of the analog wanted signal.
2. (original) System according to claim 1, wherein the compensator comprises a digital filter or an equalizer.
3. (original) System according to claim 1, wherein the analog wanted signal is a low intermediate frequency signal.
4. (original) System according to claim 1, wherein the analog wanted signal is a zero intermediate frequency signal, with a first set of analog filter analog-to-digital-converter and compensator converting and suppressing an in-phase signal and with a second set of analog filter, analog-to-digital-converter and compensator converting and suppressing a quadrature signal.
5. (original) System according to claim 1, wherein the analog filter and the compensator are matched.

6. (original) System according to claim 1, wherein the compensator is adaptive and/or comprises a control loop to avoid any matching between the analog filter and the compensator.
7. (original) System according to claim 1, further comprising an amplifier for amplifying the analog wanted signal and the analog unwanted signal.
8. (original) System according to claim 1, further comprising an amplifier for amplifying the analog output signal.
9. (original) Method of converting an analog wanted signal into a digital wanted signal and for suppressing an analog unwanted signal and comprising the steps of
at least partly suppressing the analog wanted signal and the analog unwanted signal, resulting in an analog output signal;
converting the analog output signal into a digital output signal; and
compensating the digital output signal for the at least partly suppressing of the analog wanted signal.
10. (original) Receiver comprising a tuner and a channel decoder the tuner comprising
an analog filter for at least partly suppressing an analog wanted signal and an analog unwanted signal, resulting in an analog output signal; and the channel decoder comprising
an analog-to-digital-converter for converting the analog output signal into a digital output signal; and
a compensator for compensating the digital output signal for the at least partly suppressing of the analog wanted signal.
11. (original) Tuner for use in the receiver as claimed in claim 10 and comprising the analog filter for at least partly suppressing the analog wanted signal and the analog unwanted signal, resulting in the analog output signal to be supplied to the channel decoder.

12. (original) Channel decoder for use in a receiver as claimed in claim 10 and comprising

the analog-to-digital-converter for converting the analog output signal originating from the tuner into the digital output signal; and

the compensator for compensating the digital output signal for the at least partly suppressing of the analog wanted signal.